

FEB 20 2007

REMARKS/ARGUMENTS

The above amendment and the following remarks are in reply to the non-final Office action of 11/17/2006 in the instant application. In light of this reply, reconsideration and further examination of this application are respectfully requested.

Five claims (21-24 and 31) were pending in this application. In the above amendment, none of the claims was amended or cancelled and none was added. Accordingly, five claims (21-24 and 31) remain pending for reconsideration and further examination.

On page 3 of the Office action, the Examiner rejected claims 21-24 and 31 under 35 U.S.C 102(e) as being anticipated by Seo et al. (6,445,435), stating, in pertinent part,

With regard to claim 21, figure 2A of Sea et al. discloses ... *a ladder-shaped conductive pattern (103 above and below the TFT 105, 109 to the left and right of the TFT 105) connected to the second line 101* and having a pair of parallel spaced-apart riser-like elements (109 to the left and right of TFT 105) respectively disposed on opposite sides of the first line 102.

With regard to claim 24, figure 2A of Seo et al. discloses ... *a ladder-shaped conductive pattern (103, 109) connected to the second line 101* and having a pair of parallel, spaced-apart riser-like elements 109 respectively disposed on opposite sides of the first line 102." (Emphasis added.)

In light of the remarks that follow, this rejection is respectfully traversed.

Independent claims 21 and 24 respectively include the following distinguishing limitations:

Claim 21: "... a ladder-shaped conductive pattern connected to the second line and having a pair of parallel, spaced-apart riser-like elements respectively disposed on opposite sides of the first line"

Claim 24: "... a ladder-shaped conductive pattern connected to the second line and having a pair of parallel, spaced-apart riser-like elements respectively disposed on opposite sides of the first line.

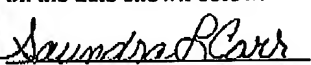
Sea et al. disclose a first line ("data bus line 102"), a second line ("gate bus line 101") intersecting the first line, a "unit pixel region" defined by the intersection of the first and second lines ('435, Figs. 2A, 2B; col. 3, lines 54-59), and "a common electrode 109 and common bus line 103 ... formed by etching a thin metal film" (*id.*, col. 4, lines 21-22). However, the common electrode 109 and bus line 103 pattern is formed above and electrically isolated from the other conductive elements of the substrate 110, including the "second line," i.e., the gate bus line 101. (*Id.*, Fig. 2B; col. 4, lines 26-28.) Accordingly, Seo et al. fail to teach or suggest the limitations of claims 21 and 24 that the ladder-shaped pattern be "connected to the second line."

App. No. 10/699,791
Amendment dated Feb. 20, 2007
Reply to Office Action of Nov. 17, 2006


Docket No. AB-1659-2C
(Ref. No. OPP 031353 US)

In light of the foregoing differences, it is respectfully submitted that independent claims 21 and 24, as well as the claims respectively dependent from them, are patentable over Seo et al., and accordingly, that all pending claims (21-24 and 31) are now in an allowable form. Applicant therefore respectfully requests that a timely Notice of Allowance be issued in this case.

If there are any questions regarding the above, the Examiner is invited to call the undersigned at the number below.

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	Feb. 20, 2007
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Respectfully submitted,


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